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PATENTS

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Yutaka TAKEISHI

Serial No. 09/739,236

Filed December 19, 2000

Confirmation No. 4462

Group 2871

Examiner Unassigned

SIGNAL PROCESSING CIRCUIT BOARD AND LIQUID CRYSTAL DISPLAY APPARATUS WITH VARIABLE RESISTOR WHICH ARE HARDLY DECREASED IN THE MECHANICAL STRENGTH WHILE ITS VARIABLE RESISTOR IS NOT LIMITED TO ONE PARTICULAR LOCATION FOR THE INSTALLATION

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents

Washington, D.C. 20231

Sir:

In compliance with Rules 1.97 and 1.98, and in fulfillment of the duty of disclosure under Rule 1.56, the accompanying documents, copies of which are attached to this statement, are made of record on the enclosed sheet.

A concise explanation of the relevance of these items is that these references were cited by the Japanese Patent Office in the corresponding Japanese application Serial No. 11-365178, filed December 22, 1999. A copy of the Japanese Official Action in which they were cited is attached hereto, with what is believed to be the pertinent portion enclosed in a wavy line. An English translation of the enclosed portion is also attached hereto.

Under the provisions of 37 CFR 1.97(e), the undersigned hereby certifies that each item of information contained in this Information Disclosure Statement was cited in a

communication from a foreign Patent Office in a counterpart foreign application not more than three months prior to the filing of this Statement.

Respectfully submitted,

YOUNG & THOMPSON

By

A handwritten signature in black ink, appearing to be 'Robert J. Patch', written over a horizontal line.

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October 22, 2002